

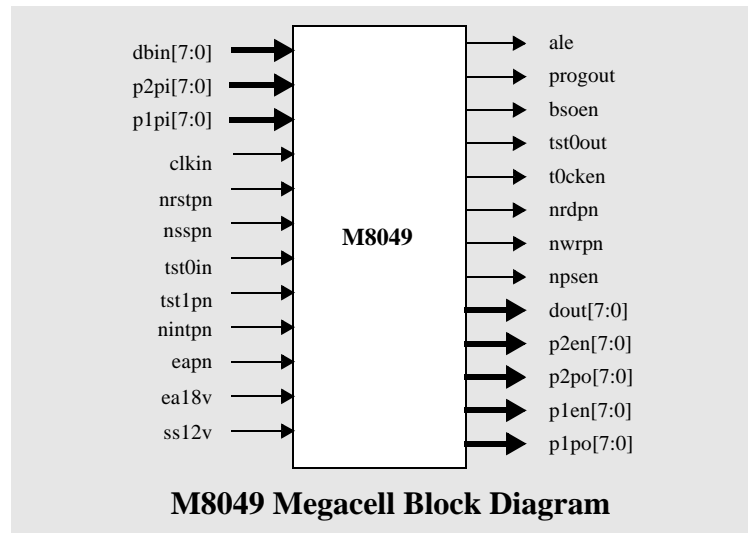
Description

M8049 megacell is a totally self sufficient microcontroller. It contains 27 I/O ports, an 8-bit Timer/Counter and two single level interrupts. This megacell is designed to be an efficient controller as well as arithmetic processor. It has extensive bit handling capacity as well as facility for both binary and BCD arithmetic. The instruction set consists of mostly single byte instructions and no instructions over two bytes in length.

M8049 emulates functionality of Intel's MCS 8049 chip. This megacell design is available as synthesizable HDL code. It can be targeted to any vendors library. The resulting netlist can be tested with available test vectors.

Features

- One 8-bit timer/counter
- 27 programmable I/O ports
- Two single level interrupts
- Can be interfaced to an external controller like 8243 to expand I/O ports
- Internal 128 bytes data memory
- Interface to address 4k bytes of program memory
- External Sync mode available
- Powerful instruction set



Services

- Design can be ported to a FPGA and a evaluation board can be provided for the customer to verify the design in his application environment
- Virtual IP Group can be develop firmware for any customer specified application based on this core
- Virtual IP Group offers turnkey development solution for any system based on this core
- Technical assistance is available to assist customers in designing a complete system based on this core